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Application Number	**09/759,414**
Filing Date	**01-13-2001**
First Named Inventor	**LI, ZHE**
Group Art Unit	**2123**
Examiner Name	**TESKA, KEVIN J**
Total Number of Pages in This Submission	
Attorney Docket Number	

ENCLOSURES *(check all that apply)*

- ☐ Fee Transmittal Form
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  - ☐ After Final
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- ☐ Extension of Time Request
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- ☒ Information Disclosure Statement
- ☐ Certified Copy of Priority Document(s)
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Firm *or* Individual name	**Zhe Li**
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Sheet 1 of 2

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Application Number	09/759,414
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Attorney Docket Number	

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		S.-Y. HUANG et al, "AutoFix: a hybrid tool for automatic logic rectification" IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, vol. 18, no. 9, September 1999, pp. 1376-1384, IEEE, U.S.A.	
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Sheet 2 of 2

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		A. KUEHLMANN et al, "Error diagnosis for transistor-level verification" Proceedings Design Automation Conference, June 1994, pp. 218-224, ACM, U.S.A.	
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		K.A. TAMURA, "Locating functional errors in logic circuits" Proceedings Design Automation Conference, June 1989, pp. 185-191, ACM, U.S.A.	

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